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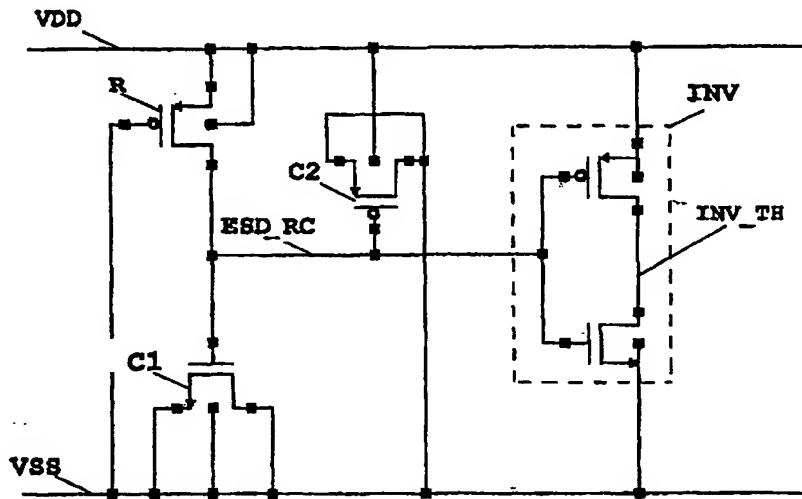
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(54) Title: CIRCUIT FOR ELECTROSTATIC DISCHARGE PROTECTION



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(57) Abstract: A circuit providing protection against electrostatic discharge (ESD) for internal elements of an Integrated Circuit (IC) is connected to a power rail (VDD) and a ground rail (VSS) and to an inverter (INV) of a clamp preamplifier. The protection circuit comprises a PMOSFET resistor (R) with a gate connected to said ground rail (VSS), a drain connected to said inverter's (INV) input node (ESD_RC), a source and a bulk connected to said power rail (VDD). The circuit also comprises an NMOSFET capacitor (C1) with a gate connected to said inverter's (INV) input node (ESD_RC), a drain, a source and a bulk connected to said ground rail (VSS), and a PMOSFET connector (C2) with a gate connected to said inverter's (INV) input node (ESD_RC), a drain, a source connected to said ground rail (VSS) and a bulk connected to said power rail (VDD).

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER 7 H02H 9/04

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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols) 7

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5946177 A (MOTOROLA, INC.) Aug. 31, 1999	1-6
A	EP 0697757 A1 (UNITED MEMORIES, INC.) 21.02.1996	1-6
A	US 5559659 A (LUCENT TECHNOLOGIES INC.) Sep. 24, 1996	1-6
A	RU 2071161 C1 (REYKEM LIMITED) 1996.12.27	1-6
A	SU 1705947 A1 (NAUCHNO-PROIZVODSTVENNOE OBEDINENIE "DALNYA CBYAZ") 15.01.1992	1-6

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